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PATENT Docket: CU-3413

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:

Kiyoshi Qoi et al.

GRP ART UNIT: 2813

Serial No: 10/693,216

Application Serial No. 10/693,216

Reply to Office Action of March 22, 2005

Ex.: David S. Blum

Filed: October 24, 2003

For:

Substrate For Carrying A Semiconductor Chip

And A Manufacturing Method Thereof

Certification under 37 C.F.R. §1.8(a)

The USPTO Central Fax No. (703) 872-9306

Date of Fax Transmittal: April 18, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above

Park, Reg. No. 55,523

The Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action dated March 22, 2005, setting a 1-month shortened statutory period for a reply ending on April 22, 2005, the pending Claims 1-20 were restricted as follows:

Claims 1-15 drawn to a semiconductor device; and

Claims 16-20 drawn to a method of making a semiconductor device.

Applicant hereby elects Claims 1-15.

Application Serial No. 10/693,216
Reply to Office Action of March 22, 2005

PATENT Docket: CU-3413

In view of the Examiner's earlier restriction requirement, Applicants retain the right to present the non-elected in a divisional application.

Respectfully submitted,

Dated: April /8, 2005

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